



**Silicon Motion, Inc.**

**SM3255EN Test Program and ISP Release Note**

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Release Date	ISP Version	ISP Check Sum	Test AP Version	Description
2010/09/30	101027-AA-	SM3255ENAAISP.BIN 0x 005E52A9	V2.03.34 v1 10/11/04	<ol style="list-style-type: none"><li>1. SM3255ENAAISP-B74 support Intel 29F64G08AATE1 (B74A) and set ECC to 64bit ECC.</li><li>2. SM3255ENAAISP support SEC 27nm TLC K9ACGD8U0M toggle mode flash.</li><li>3. SM3255ENAAISP support SEC 27nm TLC K9ABGD8U0B toggle mode flash.</li><li>4. Set the /WP high in suspend mode to fix the suspend current issue.</li><li>5. SM32Xtest.exe can support SM3255ENA1 IC version auto-load.</li></ol>
2010/11/15	101111-AA-	SM3255ENAAISP.BIN 0x 005E5500	V2.03.34 v1 10/11/04	<ol style="list-style-type: none"><li>1. Fix power-cycling FAT table block push to spare 0xBFFF..</li></ol>
2010/11/22	101118-AA-	SM3255ENAAISP-B74.BIN 0x 000532F2 SM3255ENAAISP.BIN 0x 005E5382	V2.03.34 v12 10/11/22 Build	<ol style="list-style-type: none"><li>1. Fix 5V normal power cycling fail issue.</li><li>2. SM3255ENAAISP-B74-M.BIN to support Micron B74 flash 2plane mode only.</li><li>3. Support SEC 27nm TLC 32Gb K9ABGD8U0B (EC D7 98 CE)</li></ol>



				4. Fix suddenly power-cycling FAT-Table block find last page issue (1k2)
2010/12/20	101213-AA-	SM3255ENA1ISP-B74.BIN 0x 005EFAA7 SM3255ENAAISP.BIN 0x 005CB008	V2.03.35 v7 10/12/14 Build	1. Fix 384page to keep the new bad block with Erase Good Block only function. 2. Fix Micron Flash B74A ATTO drop fail issue. 3. Fix IM Flash HDbench drop. 4. Fix 5V normal power-cycling issue. 5. Fixed the stand-by/un-config current is high issue. 6. Set the LDO setting as V1.8=0x07,V3.3=0x05 in CID: 0x16Eh 7. MP set LoopTestCount=0 in default.ini 8. MP fixed SM3255ENAA show bad block issue with ISP exist. 9. MP fixed StrongPage Table for SM3255ENA2 with Intel B74A
2011/01/05	101220-A1-	SM3255ENA1PTEST.BIN 0x 00213DD1	V2.03.35 v11 10/12/30 Build	1. Pretest fix the pair page table for 384page flash. 2. MP Add QC debug option.
2011/01/28	110127-A1	SM3255ENA1ISP.BIN 0x 005CEF4E SM3255ENA1ISP-B74A.BIN	V2.03.36 v9 11/01/27 Build	1. Fixed 5V normal Power cycling with WL issue 2. Reduced the regular device configuration is under 100ms.



		0x 005F90E4		<ul style="list-style-type: none"> <li>3. Fixed SONY PS3 compatibility issue.</li> <li>4. Modified the SEC 27nm TLC read-retry table.</li> <li>5. Fixed IM B74A USB disconnection issue.</li> <li>6. MP fixed initial fail with 192page dual die flash.</li> <li>7. MP added external execute file call function with tool initial and destroy.</li> <li>8. MP external option to bypass check USB 2.0.</li> <li>9. MP fixed to support two current limit control board.</li> <li>10. MP fixed Debug function with read memory address.</li> <li>11. MP fixed software serial number function with SM3255AB &amp; SM3257 series.</li> <li>12. MP fixed open short test flow.</li> </ul>
2011/01/31	110127-AA	SM3255ENA1ISP-B74A.BIN 0x 005F94FB	V2.03.36 v9 11/01/27 Build	<ul style="list-style-type: none"> <li>1. SM3255ENA1ISP-B74A to support LED function.</li> </ul>
2011/03/10	110225-A1 110310-A1	SM3255ENA1ISP.BIN 0x 005D3F8F SM3255ENA1ISP-B74.BIN 0x 01BE41D1	V2.03.37 v9 11/03/10 Build	<ul style="list-style-type: none"> <li>1. SM3255ENA1ISP-B74A fixed the performance drop issue on SDP.</li> <li>2. SM3255ENA1ISP-B74A fixed WinCE 6.0 compatibility issue.</li> <li>3. SM3255ENA1ISP fixed 5V normal power cycling issue when change child to FAT, last page in temp block of CE1-block can't be found</li> </ul>



				<ul style="list-style-type: none"><li>4. SM3255ENA1ISP fixed the un-recognized device and format fail after SPOR test.</li><li>5. SM3255ENA1ISP fixed compare fail and read actual data 0x00 after SPOR test.</li><li>6. MP added Ethernet communication with Hontect Auto-Handler HT-3315.</li><li>7. MP enable function - B74 flash download ISP with weak page write random data.</li><li>8. MP added ADATA function for auto-Handler HT3315 connect and agent used.</li><li>9. MP added auto-handler 3315 connect reference file for ADATA.</li><li>10. MP fixed MP tool FAT mode preload file crash with long filename or directory name.</li></ul>
2011/04/08	110322-A1	SM3255ENA1ISP.BIN 0x005E8AEF SM3255ENA1ISP-27nmES.BIN 0x005F4295	V2.03.38 v1 11/04/01 Build	<ul style="list-style-type: none"><li>1. Modify F/W to reference the spare blocks erase counts during SPOR cycling to prevent SPOT erase count high in RW area.</li><li>2. Modify F/W to set UFD into write protect mode when spare blocks number under 30 blocks.</li><li>3. Modify F/W to fix sign wrong ID into FAT blocks, prevent the FAT table wrong and make MBR broken.</li><li>4. Modify F/W to fix multiple mark bad blocks issue due to the child pair pointer was wrong.</li></ul>



				<ul style="list-style-type: none"><li>5. MP added "Fixed Capacity" function.</li><li>6. MP added two encrypt buffer with Korea customer – Rehobothtech, and TCND.</li><li>7. MP added external option for Transcend to do original format FAT32.</li><li>8. MP fixed MP tool Dialog Read CID show card mode error.</li><li>9. MP fixed MP tool Dialog Read Flash ID show card mode error.</li></ul>
2011/04/20	110418-A1	SM3255ENA1ISP-B74A.BIN 0x01BF0EAC	V2.03.38 v5 11/04/19	<ul style="list-style-type: none"><li>1. Modify F/W to support Read Only and Write Protect function.</li><li>2. Modify F/W to fix initial fail issue and modified the Intel/Micron B74 retry table.</li><li>3. Fixed MP tool auto start function fail.</li><li>4. MP fixed collection report function calculate page error with interleave mode in debug dialog.</li></ul>
2011/05/20	110517-A1	SM3255ENA1ISP-B74A.BIN 0x01BEE6AD	V2.03.40 v11 11/05/18	<ul style="list-style-type: none"><li>1. 3255ENISP enhanced the backup ISP flow for burn-in fail on disconnection issue.</li><li>2. MP modified for supporting Flash ID from 4 bytes to 6 bytes.</li><li>3. MP added option for copy compare N bytes could compare new bad block number to show fail.</li></ul>
2011/08/08	110810-A1	SM3255ENA1ISP.BIN 0x 00602430	V2.03.46 v4 11/08/05	<ul style="list-style-type: none"><li>1. SM3255ENISP Support HY 26nm MLC UBG, UCG read retry table and enhanced SLC program</li></ul>



		SM3255ENA1ISP-SA21nmMLC.BIN 0x 00604677  SM3255ENA1PTEST-HY26.BIN SM3255ENA1PTEST-HY26.BIN SM3255ENA1ISP-HY26.BIN 0x 00605C93	Build	function.  2. 3255ENISP support Samsung 21nm MLC. 3. 3255ENISP support Samsung 27nm & 21nm MLC with reliable mode. 4. 3255ENISP Support DTM v1.6 new SCSI command. 5. 3255ENISP Solve Randomize flag in pretest process. 6. MP solved "check and compare Flash ID 6 bytes problem". 7. MP solve Show ECC wrong status on CardMode button for EN series IC.
2011/08/23	110819-A1	SM3255ENA1ISP-HY26.BIN 0x00605C57 SM3255ENA1ISP.BIN 0x006024FD SM3255ENA1ISP-bucket01.BIN 0x0060F229 SM3255ENA1ISP-SA21nmMLC.BIN 00614BC9	V2.03.47 v2 11/08/17 Build	1. SM3255EN pretest solved bug within Randomize flag in pretest process. 2. SM3255ENISP fix the CV test fail issue 3. MP tool fix "Bad block information report for SM3255X series" 4. MP tool fix "Show wrong serial number for multiple devices" 5. MP tool fix "Show time value meets negative value after finish open-card.